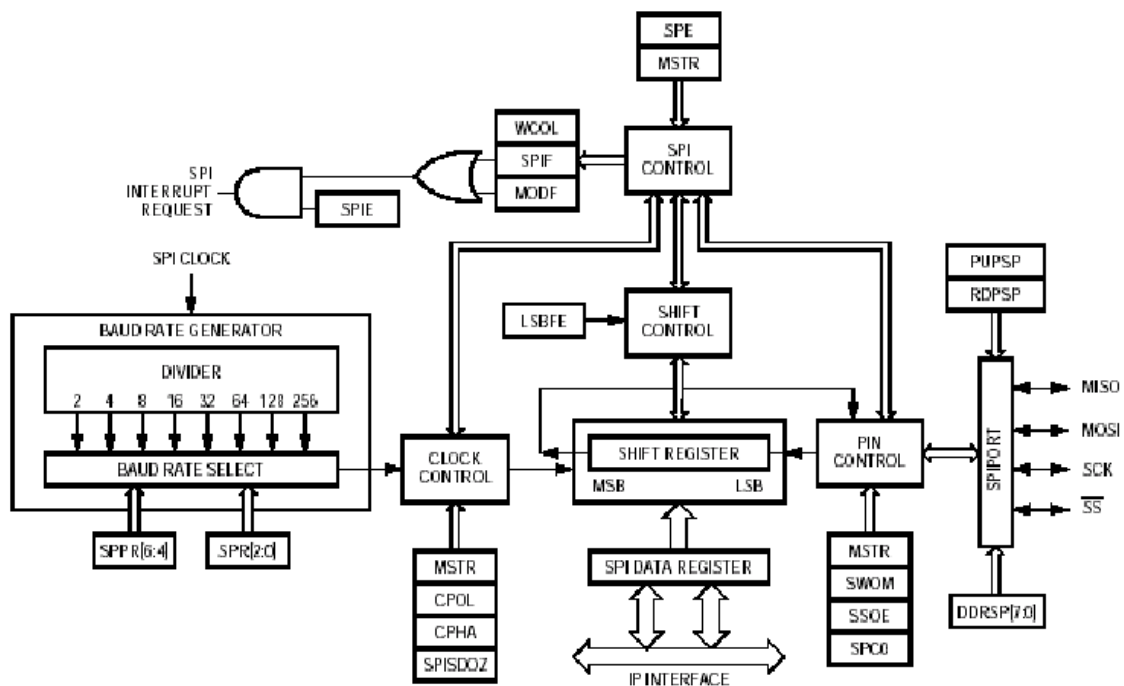# SPI Module

Summary

The serial peripheral interface (SPI) module allows full-duplex, synchronous, serial communication between the microcontroller unit (MCU) and peripheral devices. Software can poll the SPI status flags or SPI operation can be interrupt driven.

SPI Block Diagram

Features

➢ Master mode and slave mode
➢ Wired-OR mode
➢ Slave-select output
➢ Mode fault error flag with central processor unit (CPU) interrupt capability
➢ Serial clock with programmable polarity and phase
➢ Control of SPI operation during doze mode
➢ Reduced drive control for lower power consumption

Performance and Characteristics

➢ Frequency (WCS)： 100 MHz
➢ Die Size：0.029 mm2
➢ Process：0.18μm (WCS, 1.62V, 120°)

Application Examples

➢ SPI flash
➢ SPI fingerprint sensor
➢ SPI master

Availability

➢ Q2, 2003

To obtain more information about the SPI *or other C*Core^{TM} products, please contact the C*Core Technology Co., Ltd. by phone: 0512-68091377, email: support@china-core.com or web: http://www.china-core.com. C*Core™ is a trade mark of C*Core Co., Ltd.*